1P E 06 10 68726

## PATENT APPLICATION

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Yoshihiro Kizaki

Serial No.:

10/706,664

Conf. No.:

8620

Filed:

11/12/2003

For:

**SEMICONDUCTOR** 

INTEGRATED CIRCUIT

Art Unit:

2816

Examiner:

Zweizig, Jeffery Shawn

I hereby certify that this paper is being deposited with the United States Postal Service as FIRST-CLASS mail in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this date.

11/19/05

Registration No. 41,760 Storney for Applicant(s)

## **AMENDMENT A**

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action mailed January 25, 2005, please amend the application as follows: